# **Electronic Acknowledgement Receipt** FES ID: 1186127 Application Number: 10702042 Confirmation Number: 7422 Title of Invention: Synchronized communication between integrated circuit chips First Named Inventor: Jean-Paul Claveguin Customer Number: 22907 Filer: Jordan N. Bodner Filer Authorized By: Attorney Docket Number: 003921.00149 Receipt Date: 05-SEP-2006 Filing Date: 06-NOV-2003 Time Stamp: 17:26:21 Application Type: Utility International Application Number:

## Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$450
RAM confirmation Number	508
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

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#### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1	Response to Election / Restriction Filed	00392100149_response_to_ restriction_requirement.pdf	71188	no	1		
Warnings:							
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8133	no	2		
Warnings:							
Information:							
		Total Files Size (in bytes):	79321				

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### New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

#### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.